



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: BLASI, et al.

For: TEST BENCH GENERATOR FOR INTEGRATED CIRCUITS, PARTICULARLY MEMORIES

Mail Stop PATENT APPLICATION
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PATENT APPLICATION TRANSMITTAL LETTER

Transmitted herewith for filing, please find the following:

X Specification, claims and abstract of the above-referenced patent application totaling 117 pages

X 20 sheet(s) of (X formal/ informal) drawings comprising 14 Figures

X Combined Declaration and Power of Attorney (unsigned) (3 pages)

 An Information Disclosure Statement Under 37 C.F.R. § 1.97(b) along with form PTO-1449 and a copy of each reference identified therein

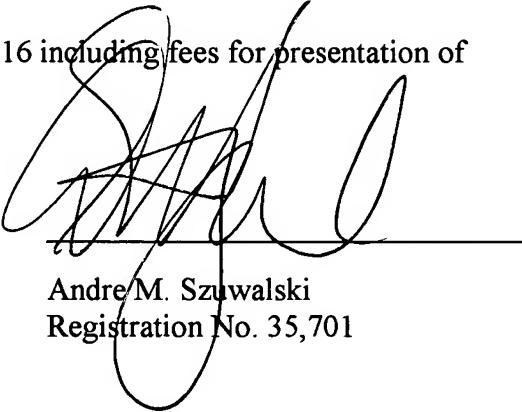
X Applicant(s) claim(s) priority to EP Patent No. 02425415.3 as filed on June 25, 2002 in accordance with 35 U.S.C. § 119.

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- The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account No. 10-0447.
- Any patent application processing fees under 37 CFR 1.17 and under 37 CFR 1.20(d).
- The issue fee set in 37 CFR 1.18 at or before mailing of the Notice of Allowance, pursuant to 37 CFR 1.311(b).
- Any filing fees under 37 CFR 1.16 including fees for presentation of extra claims.



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